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Bib Data Sheet

CONFIRMATION NO. 6895

SERIAL NUMBER 10/814,532	FILING DATE 03/31/2004 RULE	CLASS 257	GROUP ART UNIT 2815	ATTORNEY DOCKET NO. 254-094-PCT-2- USCIP-1
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APPLICANTS

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**** CONTINUING DATA *******

This application is a CIP of PCT/US03/29000 09/15/2003
 which is a CON of 10/453,398 06/03/2003
 which is a CIP of 10/005,581 10/26/2001 PAT 6,576,992

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**** FOREIGN APPLICATIONS *******

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IF REQUIRED, FOREIGN FILING LICENSE GRANTED

**** 06/15/2004**

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY TX	SHEETS DRAWING 34	TOTAL CLAIMS 19	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after				
Verified and Acknowledged	Examiner's Signature <i>James W. Cady</i>	Initials <i>gbe</i>		

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TITLE

Integrated circuit stacking system and method

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees (Filing)

FILING FEE RECEIVED 856	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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